ABSTRACT

Circuit and method for hiding peer devices from a computer host are provided. The devices and host share a common electrical bus, e.g., a PCI bus. The method allows to generate a signal indicative of the presence of a first peer device. The method further allows to hide a second peer device from the host so that the second device is controlled by the first peer device whenever the signal indicates the presence of the first device.

ORL1 #637922 v1

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